

## Syllabus Progress Report upto 31/03/2022


Course Name:	Concrete Technology	Course Code:	CE(PC)404
Teacher Name:	RAJEN KOLEY	Teacher Employee Code:	5004
Department Name:	CIVIL ENGINEERING	Semester:	4TH
LTP Structure:	2_1_0		
Total No. of classes Allotted:	40		
Total No. of classes conducted:	12		
% of syllabus covered:	25		
<b>Module No.</b>	<b>Topics Covered</b>	<b>Methodology Used</b>	<b>No. of Lectures conducted</b>
Module 1	Cement: Manufacturing of cement, Oxides composition of cement and the calculation of compounds, Heat of hydration, Types of cement- OPC, RPC, Low heat cement, PPC, PSC, Sulphate resisting cement, High Alumina cement, Expansive cement, White cement; Test on cement- fineness, consistency, initial setting time & final setting time, soundness test, strength test, specific gravity of cement, storage of cement.	SMART BOARD & PPT	5
Module 2	Aggregates: Classification, Grading, alkali-aggregate reaction, deleterious substances in aggregates, physical properties, testing of aggregates- fineness modulus, bulking, specific gravity, sieve analysis, flakiness & elongation index. Quality of Water for mixing and curing - use of sea water for mixing concrete.	SMART BOARD & PPT	7
Signature of the Faculty Member <i>Rajen Koley</i> 22/4/22		Signature of HOD <i>M. J. ...</i>	


## Syllabus Progress Report for Computer Network (EC-602): Upto 31.03.2022

Course Name:	Computer Network	Course Code:	EC 602
Teacher Name:	Anindita Sinha	Teacher Employee Code:	8009
Department Name:	ECE	Semester:	6th
LTP Structure:	3-0-2		
Total No. of classes Allotted:	42		
Total No. of classes conducted:	20		
% of syllabus covered:	40%		
<b>Module No.</b>	<b>Topics Covered</b>	<b>Methodology Used</b>	<b>No. of Lectures conducted</b>
1	Explain Digital Communication with block diagram and introduction to network, Working Operation of different OSI layer. Differences between OSI and TCP layer, Analog & Digital Electronics, Communication Basics, Different Network Connections, Different network topologies, Different ways of data transmission, Unguided and Guided media.	Chalk and Blackboard	11
2	Function of Network Layer, Logical addressing, IP addressing-different notations, conversion, different classes, mask, Ipv6, NetID and Host ID, Default mask and subnet mask and error detection and correction, subnet.	Chalk and Blackboard	5
2	Explain in detail on Ipv4 and Ipv6 with mathematical example; Discussion on mapping procedure; ARP and RARP with proper packet format and mathematical example and operation	Chalk and Blackboard	5
Signature of the Faculty Member <i>Dr. Sinha 21.4.22</i>		Signature of HOD <i>[Signature] 21.4.22</i>	

## Syllabus Progress Report upto 31.03.2022

<b>Course Name:</b>		LINE COMMUTATED AND ACTIVE PWM RECTIFIERS		<b>Course Code:</b>		PE-EE 801A
<b>Teacher Name:</b>		Subhajit Roy		<b>Teacher Employee Code:</b>		9010
<b>Department Name:</b>		Electrical Engineering		<b>Semester:</b>		8th
<b>LTP Structure:</b>		3-0-0				
<b>Total No. of classes Allotted:</b>		36				
<b>Total No. of classes conducted:</b>		18				
<b>% of syllabus covered:</b>		36%				
<b>Module No.</b>	<b>Topics Covered</b>		<b>Methodology Used</b>		<b>No. of Lectures conducted</b>	
1	<b>Diode rectifiers with passive filtering:</b> Half-wave diode rectifier with RL and RC loads; 1-phase full-wave diode rectifier with L, C and LC filter; 3-phase diode rectifier with L, C and LC filter; continuous and discontinuous conduction, input current wave shape, effect of source inductance; commutation overlap		Face to Face, ICT		8	
3	<b>Multi-Pulse converter:</b> Review of transformer phase shifting, generation of 6-phase ac voltage from 3-phase ac, 6-pulse converter and 12-pulse converters with inductive loads, steady state analysis, commutation overlap, notches during commutation.		Face to Face, ICT		5	
4	<b>Single-phase ac-dc single-switch boost converter:</b> Review of dc-dc boost converter		Face to Face, ICT		1	
5	<b>Ac-dc bidirectional boost converter:</b> Review of 1-phase inverter and 3-phase inverter		Face to Face, ICT		4	

  
 08.04.22  
 Signature of the Faculty Member

Head of the Department  
 Department of Electrical Engineering  
 Siliguri Institute of Technology  
  
 Signature of HOD